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To: Office of Initial Patent
 Examination's Filing Receipt
 Corrections

From: Mikio Ishimaru

Fax: 703-746-9195

Pages: 8

Phone:

Date: February 5, 2004

Re: U.S. Patent Application Serial CC:
 No. 10/689,923

☒ Request for Correction of Filing Receipt

☐ Information

☐ Other

IMPORTANT

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Dear Sir/Madam:

Attached are a Transmittal and Request for Correction of Filing Receipt, Original Filing Receipt with Corrections hand written, and Original Data Sheet, for U.S. Patent Application Serial No. 10/689,923 (attorney docket no. 1016-024).

Respectfully submitted,



Mikio Ishimaru
 Reg. No. 27,449

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):	Tommy Lai et al.	Confirmation No.:	4860
Application No.:	10/689,923	Group Art Unit:	2812
Filed:	10/17/2003	Examiner:	Unknown
Title:	INTEGRATED CIRCUIT WITH PROTECTED IMPLANTATION PROFILES AND METHOD FOR THE FORMATION THEREOF		

TRANSMITTAL AND REQUEST FOR CORRECTION OF FILING RECEIPT

Office of Initial Patent Examination's
Filing Receipt Corrections
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir/Madam:

Petitioner hereby requests a correction in the Filing Receipt of the above-referenced patent application. Enclosed is a copy of the Filing Receipt with the changes noted thereon. There is an error with respect to the following, which is incorrectly entered.

Error inCorrect data

Applicant (2)


Pradeep Ramachandramurthy Yelehanka

Transmitted herewith are the documents for the above-identified application:

- ☒ Filing Receipt with Correct name hand written
- ☒ Copy of Data Sheet filed with Application on 10/17/03
- ☒ Fax Cover Sheet
- ☒ No additional fee is required

At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 50-0374 pursuant to 37 CFR 1.25.

Respectfully submitted,


Mikio Ishimaru
Reg. No. 27,449
January 5, 2004

The Law Offices of Mikio Ishimaru
1110 Sunnyvale-Saratoga Road, Suite A1
Sunnyvale, CA 94087-2554

CERTIFICATE OF FAXING/TRANSMISSION (37 C.F.R.1.8a)

I hereby certify that this correspondence is on the date shown below being:

- ☒ Faxed to the: Office of Initial Patent Examination's Filing Receipt Corrections, Commissioner for Patents
P.O. Box 1450, Alexandria, VA 22313-1450

Date: 2/5/04

Signature


Justin Zahrt



UNITED STATES PATENT AND TRADEMARK OFFICE

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/689,923	10/17/2003	2812	856	1016-024	4	20	4

CONFIRMATION NO. 4860

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RECEIVED

JAN 29 2004

MIKIO ISHIMARU

FILING RECEIPT

OC000000011768202

Date Mailed: 01/27/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Tommy Lai, Kowloon, HONG KONG;
Pradeep ~~Ramachandramurthy~~ Yelehanka, Singapore, SINGAPORE;
Jia Zhen Zheng, Singapore, SINGAPORE;
Weining Li, Shanghai, CHINA;

Ramachandramurthy

Assignment For Published Patent Application

CHARTERED SEMICONDUCTOR MANUFACTURING LTD., Singapore, SINGAPORE;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 01/26/2004

Projected Publication Date: 04/21/2005

Non-Publication Request: No

Early Publication Request: No

Title

Integrated circuit with protected implantation profiles and method for the formation thereof

Preliminary Class
438

**LICENSE FOR FOREIGN FILING UNDER
Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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